Electronic Acknowledgement Receipt		
EFS ID:	5821105	
Application Number:	10578005	
International Application Number:		
Confirmation Number:	5213	
Title of Invention:	Phase Error Correction Circuit	
First Named Inventor/Applicant Name:	Tatsuji Ishii	
Customer Number:	20277	
Filer:	Michael E. Fogarty/3328/Matilda Mason	
Filer Authorized By:	Michael E. Fogarty	
Attorney Docket Number:	071971-0551	
Receipt Date:	03-AUG-2009	
Filing Date:	03-MAY-2006	
Time Stamp:	20:56:07	
Application Type:	U.S. National Stage under 35 USC 371	

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		071971-0551 Response.pdf	151703	ves	7
			30038d8857ef518f2a26e4c786ab381a8fb2 0597	'	,

Document Description	Start	
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Amendment/Req. Reconsideration-After Non-Final Reject	1	1
Specification	2	2
Applicant Arguments/Remarks Made in an Amendment	3	5
Drawings-only black and white line drawings	6	7
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.